

NonMetricSpaceLib Property Pages

Configuration: Active(Debug) Platform: Active(x64) Configuration Manager...

- ▶ Common Properties
- ▲ Configuration Properties
 - General
 - Debugging
 - VC++ Directories
 - ▲ C/C++
 - General
 - Optimization
 - Preprocessor
 - Code Generation**
 - Language
 - Precompiled Headers
 - Output Files
 - Browse Information
 - Advanced
 - All Options
 - Command Line
 - ▶ Librarian
 - ▶ XML Document Generator
 - ▶ Browse Information
 - ▶ Build Events
 - ▶ Custom Build Step
 - ▶ Code Analysis

Enable String Pooling	No (/Gm-)
Enable Minimal Rebuild	Yes (/EHsc)
Enable C++ Exceptions	No
Smaller Type Check	Both (/RTC1, equiv. to /RTCsu) (/RTC1)
Basic Runtime Checks	Multi-threaded Debug DLL (/MDd)
Runtime Library	Default
Struct Member Alignment	Enable Security Check (/GS)
Security Check	
Enable Function-Level Linking	
Enable Parallel Code Generation	
Enable Enhanced Instruction Set	Advanced Vector Extensions 2 (/arch:AVX2)
Floating Point Model	Streaming SIMD Extensions (/arch:SSE)
Enable Floating Point Exceptions	Streaming SIMD Extensions 2 (/arch:SSE2)
Create Hotpatchable Image	Advanced Vector Extensions (/arch:AVX)
	Advanced Vector Extensions 2 (/arch:AVX2)
	No Enhanced Instructions (/arch:IA32)
	Not Set
	<inherit from parent or project defaults>

Enable Enhanced Instruction Set
 Enable use of instructions found on processors that support enhanced instruction sets, e.g., the SSE, SSE2, AVX, and AVX2 enhancements to IA-32; AVX and AVX2 to x64. Currently /arch:SSE and /arch:SSE2 are only available when building for the x86 architecture. If no option is specified, the compiler will use instructions found on processors that support SSE2. Use of enhanced instructions ca...