

Initial State

CF

X

1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 1 1 **1**

After 1-bit SHL/SAL instruction

1

0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 1 1 **1** 0

After 1-bit SHL/SAL instruction

0

0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 1 1 **1** 0 0 0 0 0 0 0 0 0 0 0 0