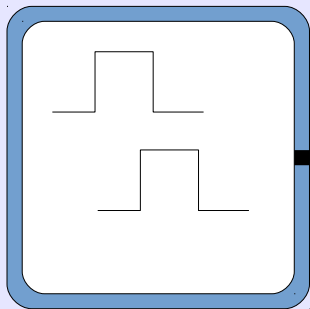


# Simulation



Waveform viewer

xCORE  
Ports

SPI  
Master

Application

xCORE device